

# PATENT ABSTRACTS OF JAPAN

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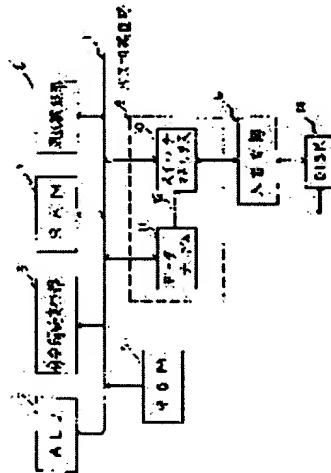
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## (54) MICROCOMPUTER

### (57)Abstract:

**PURPOSE:** To read out the contents of a converting part to be ciphered to the outside by providing a bus switch circuit which ciphers the data and the programs based on the command of a data arithmetic part and the data stored in a storage part between the data arithmetic part/storage parts and an input/output part.

**CONSTITUTION:** A data arithmetic part ALU 2 is provided together with a ROM 5 and a RAM 7 which store the data and the programs, and an input/output part 6. Then a bus switch circuit 9 which ciphers the data and the programs is provided between the ALU 2/ROM 5 and RAM 7 and the part 6. The circuit 9 switches the arrangement of the bit trains based on the command of the ALU 2 and the data and the programs of the ROM 5 and the RAM 7 and ciphers the data and the programs. These ciphered data and programs are outputted to the outside through the part 6 and at the same time the ciphered data and programs inputted from the outside are decoded and sent to the ALU 2, the ROM 5 and the RAM 7 respectively. Thus it is not required to especially input the ciphered programs and data. Furthermore the contents of the circuit 9 can be read out and sent to the outside.



## LEGAL STATUS

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